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**Figure 1. Programming logic scheme for SIPS-VSM Searching**

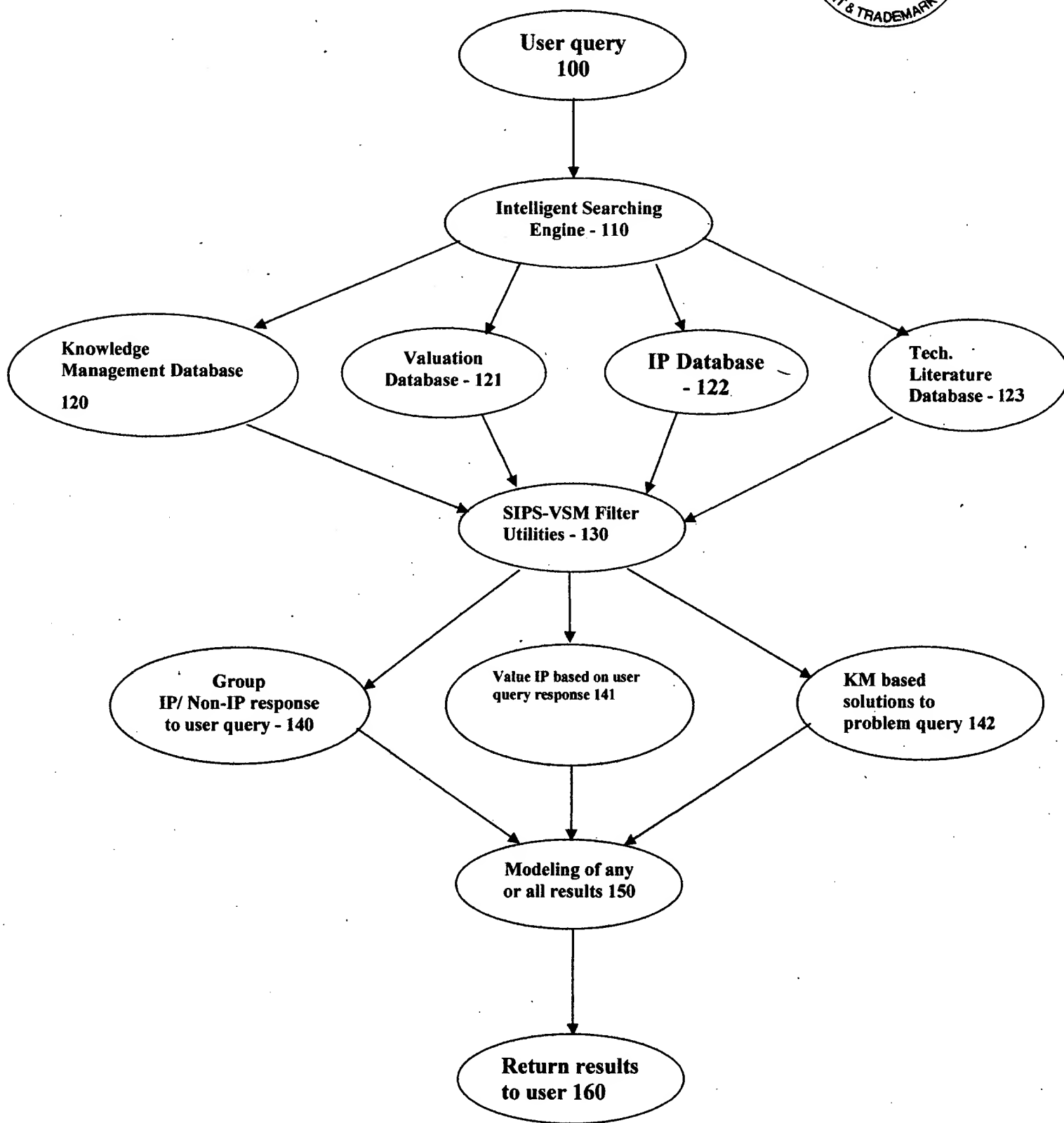


Figure 2. Overall logic flow for SIPS-VSM

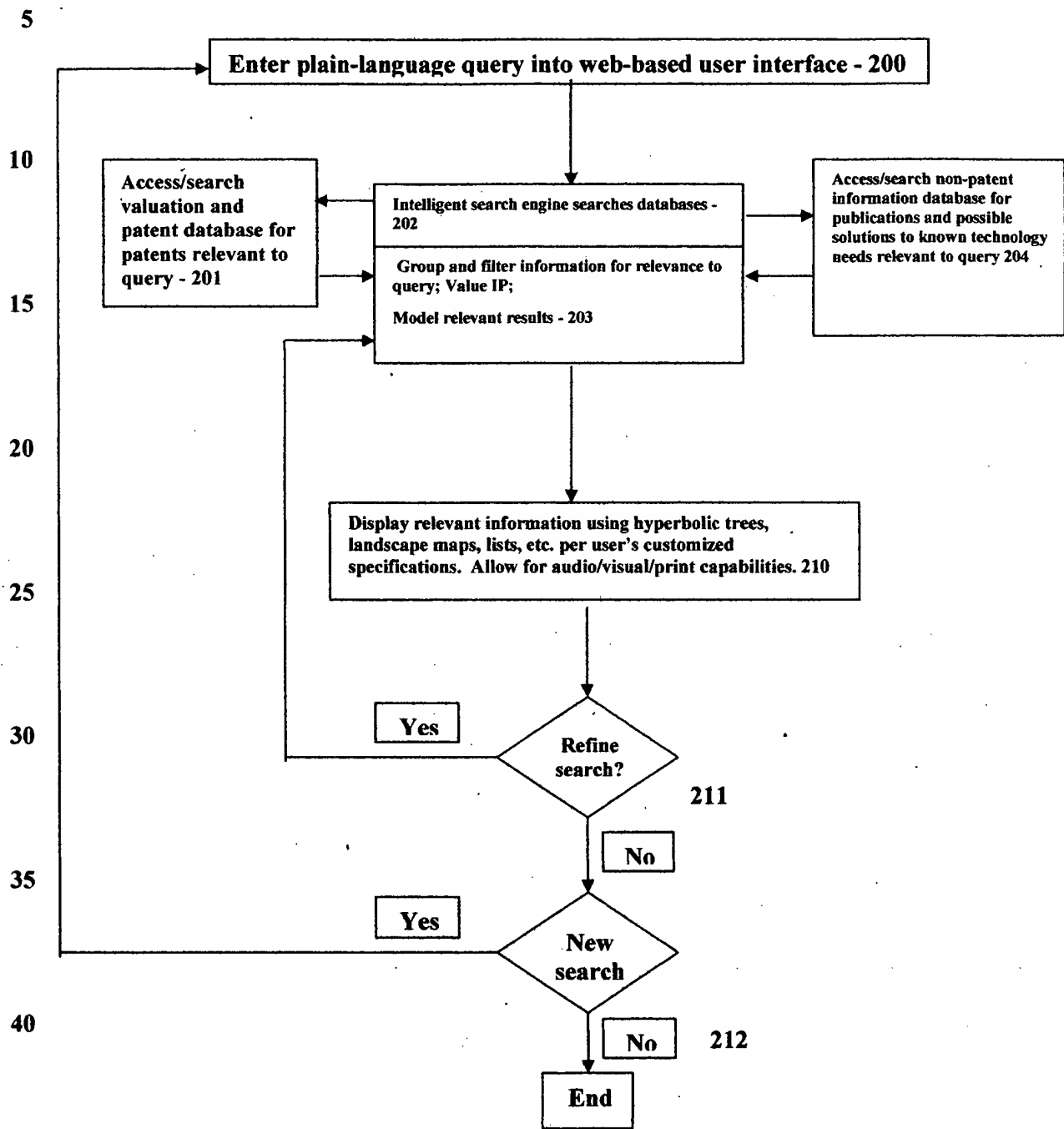
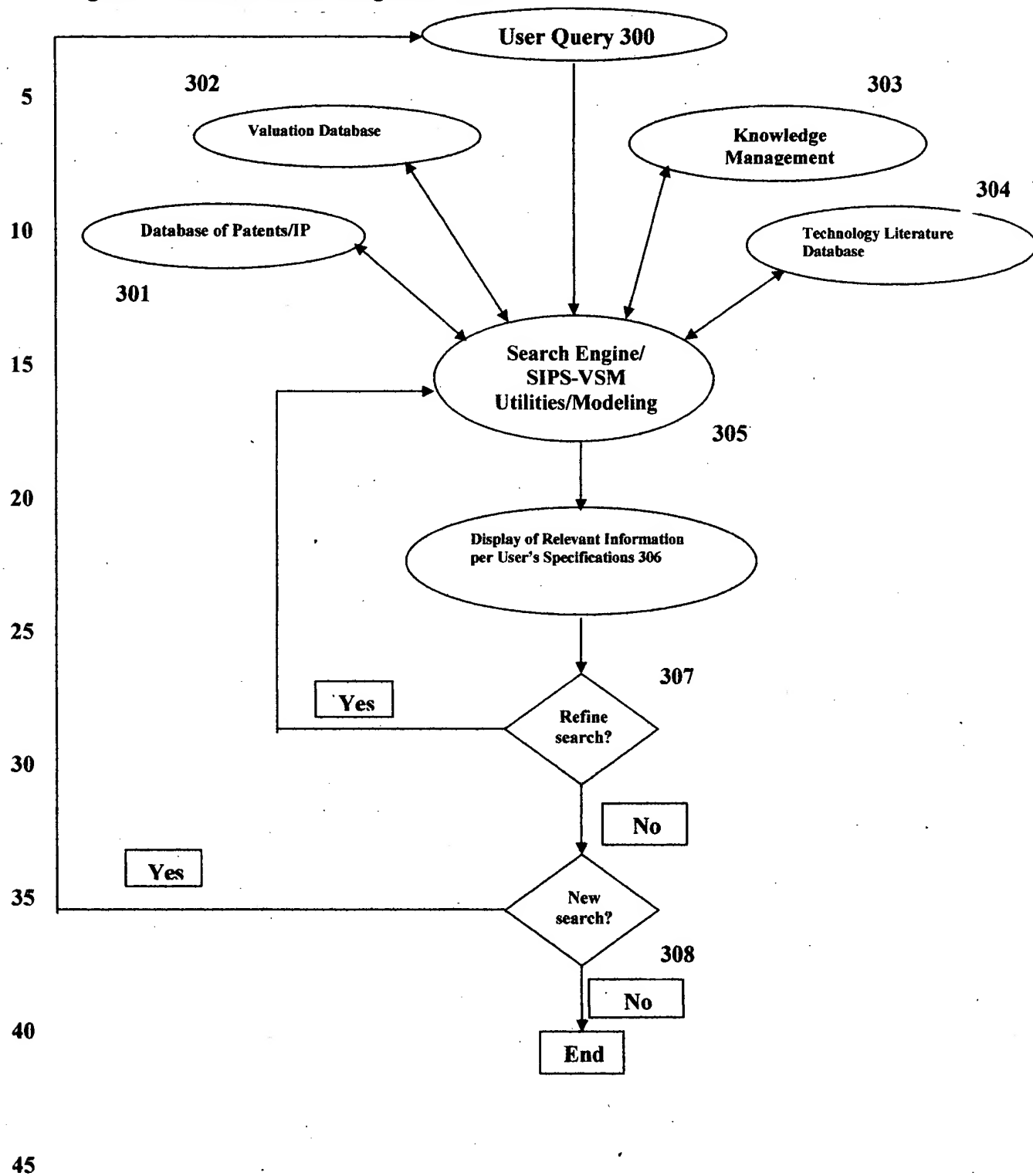


Figure 3. Database search using SIPS-VSM



**Figure 5. Computer -Based Hierarchy for SIPS-VSM**

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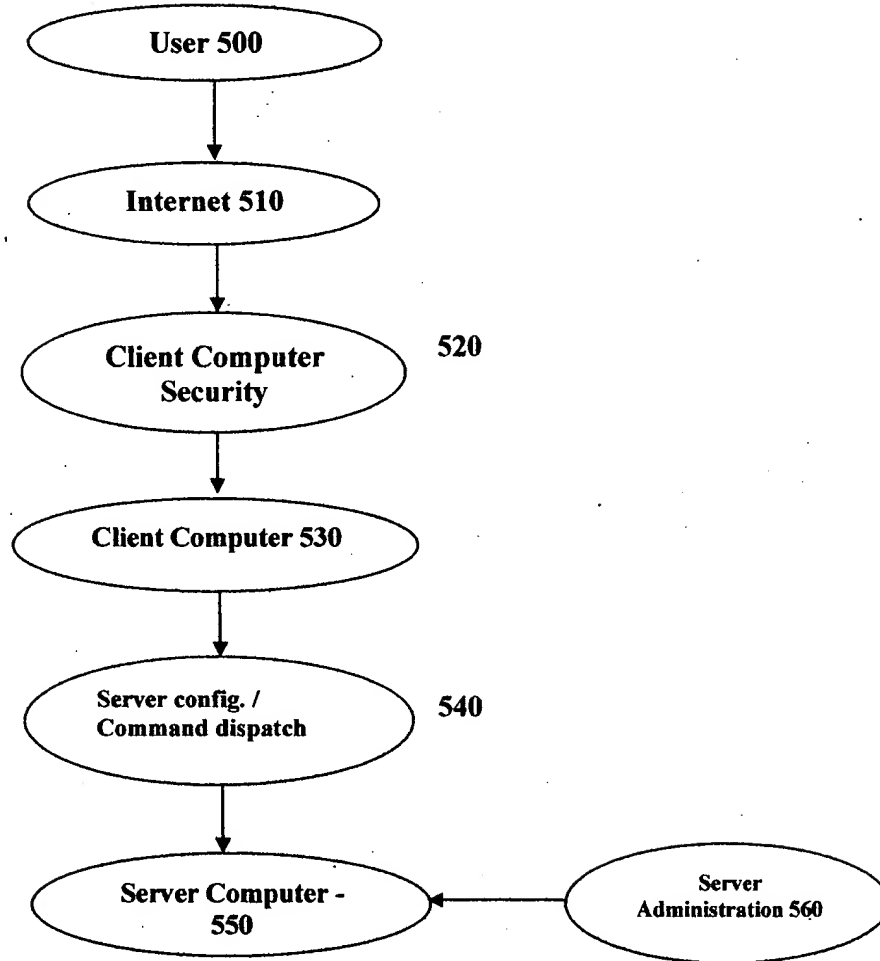
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**Figure 6: Sample IP Valuation Map Indicating Specific Patent Values within Associated Technology Areas**

